

Parallelizing Crochemore's Repetitions Algorithm to Compute Runs in Strings

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Background

- A run, a maximal fractional repetition in a string was conceptually introduced by Main in 1989[4].
- R. Kolpakov and G. Kucherov[5] showed how to compute all the runs from leftmost runs in 2000.
- A typical linear time algorithm for computing runs:
suffix array \Rightarrow L-Z factorization \Rightarrow leftmost runs \Rightarrow all runs
- The linear-time algorithms for computing runs are not very conducive to parallelization mainly because the suffix tree or suffix array rely on recursion.
- Though Crochemore's repetitions algorithm has complexity of $O(n \log n)$, its strategy of repeated refinements of classes of equivalence, a process can be naturally parallelized.

Repetition

Definition

(s, p, e) is a repetition in x iff

$x[s+i] = x[s+p+i] = \dots = x[s+(e-1)p+i]$ for $0 \leq i < p$ and $e \geq 2$. s is the starting position, p is the period, e is the exponent (or power), and $x[s..s + p - 1]$ the generator of the repetition. The generator must be irreducible (not a repetition).

		1	2	3	4	5	6	7	8	9	10
x	=	b	a	b	a	a	b	a	a	b	b

The repetition can be encoded as (s, p, d) , where d is the ending position of the repetition, with $d = s + ep - 1$.

Run

Definition

(s, p, e, t) is a run in x , if

- ① for every $0 \leq i \leq t$, $(s + i, p, e)$ is a maximal repetition, and
- ② either $s = 0$ or $x[s - 1] \neq x[s + p - 1]$ (the run cannot be extended to the left), and
- ③ either $s + ep + t > n$ or $x[s + (e - 1)p + t] \neq x[s + ep + t]$ (the run cannot be extended to the right).

	1	2	3	4	5	6	7	8	9	10
$x =$	b	a	b	a	a	b	a	a	b	b

A run (s, p, e, t) can be encoded as (s, p, d) where d is the end position of the run, with $e = (d - s + 1)/p$ and $t = (d - s + 1) \% p$.

Crochemore's Repetitions Algorithm

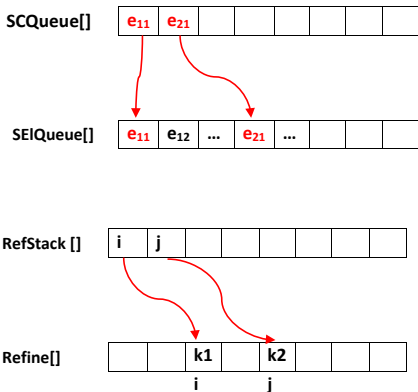
- 1981 Crochemore designed the first $O(n \log n)$ algorithm to compute all the repetitions in a string[3]. The main ideas of his approach is to refine the indices of the string into several equivalent classes at each level.
- We say two indices at level l are equivalent if two identical substring of length l start there. i.e. $f = abcab \{1,4\}_{ab}$ at level 2
- After initial refinement, the original input string need not be accessed anymore, the rest refinements use other classes from previous level and only those so-called small classes, which brings the worst-case complexity to $O(n \log n)$.

FSX03 & C2-K

- Franek et. al. gave a most memory efficient implementation of Crochemore's algorithm referred to as **FSX03**[2].
- In [1], algorithm C was the best extension algorithm to compute in terms of performance though it requires an extra $O(n \log n)$ memory space. Its variant **C2-K** was introduced to reduce the memory requirement.
- Parallelize FSX03 & C2-K to compute runs within **shared memory model**.

FSX03 Overview

- FSX03 implements the refinement step by traversing and processing the all the elements in the small classes. For each element e , $e - 1$ gets refined.
- Refine current level of classes from previous level of classes. However it's too expensive to keep two levels, a notion of “snapshot” is used to keep the small classes from previous level.
- When refine a class it involves moving the element from its original class to a new class or leaving it in place. FSX03 uses `Refine[]` and `RefStack[]` to keep track of these classes. They are cleared after processing each small class.



Small Classes: $\{e_{11}, e_{12}, \dots\}$ $\{e_{21}, e_{22}, \dots\}$...

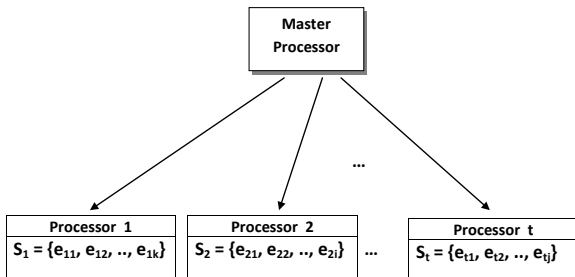
Classes: $C_i = \{e_{11} - 1, \dots\}$ $C_j = \{e_{12} - 1, \dots\}$...

$C_{k1} = \{e_{11} - 1\}$ $C_{k2} = \{e_{12} - 1\}$

Alternative 1

Assign each small class to a processor to process refinements simultaneously.

- Extra memory for `Refine[]` and `RefStack[]` required for each processor.
- Less processors required.



Allocate memory for Refine [] and RefStack []:

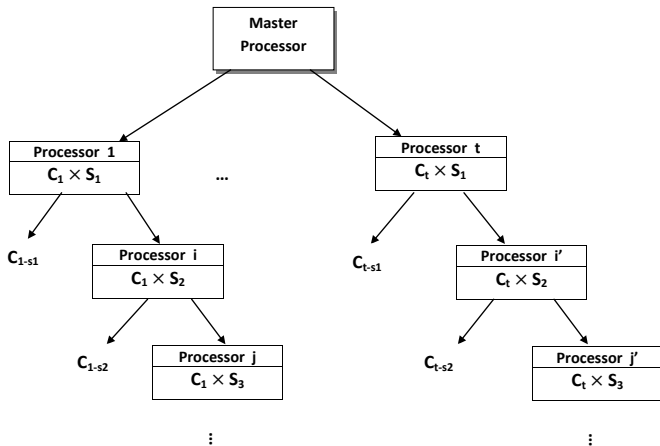
- **Static:** size of n for both
- **Dynamic:** size of the assigned small class to RefStack [] and the largest class number to Refine []

Alternative 2

Each class is refined by all small classes, assign the refinement of each class to a processor.

- No extra memory required.
- More processors required.

Refine every class by all the small class: S_1, S_2, \dots



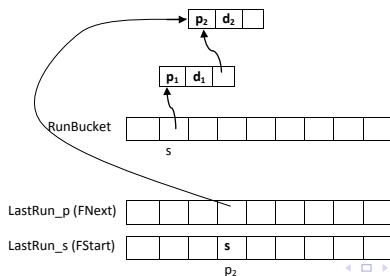
Remark

- Mutual exclusion locking for both read and write required for critical routines i.e. `AddToClass` or `RemoveFromClass`.
- Other steps in FSX03 could potentially be parallelized. i.e. computation of the level 1 can be done by partitioning the string and processed by multiple processors.

C Overview

C is an extension algorithm to compute runs:

- 1 **Collect** all the repetitions into an array of linked lists based on their starting positions.
- 2 **Traverse** all the repetitions and consolidates the “nearby” repetitions with the same period into runs.



C2-K Overview

C2-K is a variant of C, and it's designed for bring down the memory requirement of C.

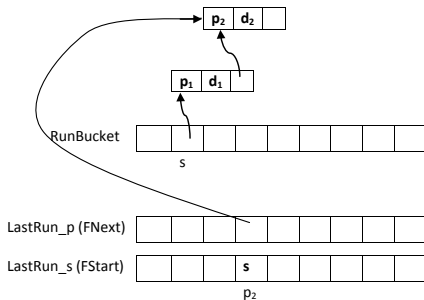
- 1 Partially consolidates repetitions into runs when putting them into the buckets. For a repetition with period $p \leq K$ and start s , we check p buckets to the left and to the right of s ; for $p > K$, we check K buckets.
- 2 Traverses and consolidates the repetitions with periods $p > K$ as C2-K guarantees that all repetitions up to period K have been consolidated into runs before the final sweep.

Description

- Break down consolidation work in terms of periods and each processor is assigned with a ranges of periods. Every processor traverse the buckets and consolidate only the repetitions with assigned periods.
- The range of the periods of string for C2-K is $(K + 1, \lfloor n/2 \rfloor)$.
 - Equally distributes over P processors. $\lceil (n/2 - (K + 1) + 1) / P \rceil$ number of periods are assigned to each processor.
 - Or assign a fixed number periods t to each processors until all the periods have been done.

Data Structure

There is **NO** extra data structure required for the parallelizing C2-K.








Remark

- No additional space is required.
- No extra actions such as locking are needed.
- Might increase the overall complexity, however, overall execution time should not be affected.

Summary & Future Work

- We have investigated parallelization of Crochemore's repetitions algorithm to compute runs within **shared memory model**.
- We are currently working on **implementation** for a multiple-core machine platform and extensive **testing** against various types of strings.
- We intend to investigate all aspects of parallelization of the extended Crochemore's algorithm within **distributed memory model**.
- We plan on using **SHARCNET** as the hardware platform for the implementation of the distributed memory parallel version of the algorithm.

References

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THANK YOU!